	Type	Hits	Search Text
1	BRS	4	((margin OR extreme) ADJ test\$3) AND computer AND (BMC OR (baseboard AJD management ADJ controller)) AND frequenc\$3 AND ((digital ADJ parameter) WITH (frequency ADJ synthesizer)) AND (communicat\$4 OR transmit\$4 OR receiv\$3)
2	BRS	4	((margin OR extreme) ADJ test\$3) AND computer AND (BMC OR (baseboard AJD management ADJ controller)) AND frequenc\$3 AND ((digital ADJ parameter) AND (frequency ADJ synthesizer)) AND (communicat\$4 OR transmit\$4 OR receiv\$3)
3	BRS	4	<pre>((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND ((digital ADJ parameter) AND (frequency ADJ synthesizer)) AND (communicat\$4 OR transmit\$4 OR receiv\$3)</pre>
4	BRS	6	<pre>((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND ((digital ADJ parameter)) AND (communicat\$4 OR transmit\$4 OR receiv\$3)</pre>
5	BRS	228	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND (communicat\$4 OR transmit\$4 OR receiv\$3)
6	BRS	21	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND communicat\$4 AND transmit\$4 AND receiv\$3 AND diagnos\$4
7	BRS	80	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND communicat\$4 AND transmit\$4 AND receiv\$3
8	BRS	11	((frequenc\$3 ADJ (margin OR extrem\$3)) WITH test\$3) AND server
9	BRS	4 9	((frequenc\$3 ADJ (margin OR extrem\$3)) WITH test\$3)

	Туре	Hits	Search Text
10	BRS	39	((integrated ADJ circuit) OR IC) AND amplif\$3 AND switch\$3 AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface) AND operation\$3
11	BRS	1	((integrated ADJ circuit) OR IC) AND amplif\$3 AND (operational ADJ characteristic) AND switch AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface)
12	BRS	39	((integrated ADJ circuit) OR IC) AND amplif\$3 AND switch\$3 AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface)
13	BRS	38	((integrated ADJ circuit) OR IC) AND amplif\$3 AND switch AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface)